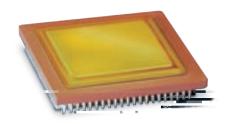
BRE440A™ RADHARD SOC CPU



The BRE440A Central Processing Unit (CPU) is a fully radiation hardened implementation of the PowerPC 440 processor core in a true System-On-A-Chip (SOC) design, including floating point unit. It meets the requirements for a wide variety of high reliability space and missile applications.

The BRE440A SOC CPU integrates a mix of peripheral controllers by implementing IBM's h On-chip peripherals reduce chip count and improve system throughput. The 440 core con provides an ideal foundation for compact systems

The BRE440A is the next generation of the BRE440 SOC CPU architecture which has extensive flight heritage and is currently on-orbit spacecraft. This revision is designed to provid hardened processing available for spacecraft, mis





mix



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